

Grid Tie Inverter in Photovoltaic System for Residential Application

Tan Kheng Kwang¹, Syafrudin Bin Masri²

¹*School of Electrical and Electronic Engineering*

University Sains Malaysia, Engineering Campus

14300 Nibong Tebal, Seberang Perai Selatan, Pulau Pinang, Malaysia

Email: edwardtan_tchtch@yahoo.com

²*School of Electrical and Electronic Engineering*

University Sains Malaysia, Engineering Campus

14300 Nibong Tebal, Seberang Perai Selatan, Pulau Pinang, Malaysia

Tel: +604-5996018, Email: syaf@eng.usm.my

Abstract

This paper presents a grid tie inverter (GTI) in photovoltaic, PV system for residential application. The proposed GTI will be switched with a combination switching strategy of square wave and the sinusoidal pulse width modulation, SPWM. The combination switching strategy will be discussed and the performance of the inverter also will be simulated under grid tie condition in SIMULINK. Moreover, the strategy of sending power into the grid also will be discussed.

1. Introduction

With the increase of human population of the world and the rapid growth of the global economic, the demand of the energy resources especially electricity faced a tremendous increase. According to the U.S. Energy Information Administration in the International Energy Outlook 2009 said that the global electricity generation is predicated to be increase from 18 trillion kWh in 2006 to 23.2 trillion kWh in 2015 and will further increase to 31.8 trillion kWh for the next 5 years [1]. With such rapid increase in electricity demand, using conventional and classical electricity generation by burning the fossil fuel to fulfill the electricity demand is no longer able fulfill the demand. Besides that, burning the fossil fuel will result in environmental problems. The emission of carbon dioxide, methane and other greenhouse gasses will result in global warming problem. Moreover, after ratify of the Kyoto and Copenhagen accord by most of the countries in the world, this conventional method is now no longer a legal way to generate electricity.

This has given an opportunity for the renewable energy resources and technology to take over the electricity generation process. One of the renewable and believe to be unlimited energy resources is from the Great Sun or commonly known as the solar energy. Photovoltaic, PV is a technology first observes and develops back in 1839 by Alexandre-Edmond Becquerel that will convert the solar energy to electricity. Since then, the PV technology had come across with tremendous

change and development, starting from few watt stand alone system to few mega watt grid connected system.

The conventional PV system is as shown in figure 1. The PV array or module will produce an inconstant DC voltage. This inconstant DC voltage will then pass through a constant output voltage with maximum power point tracker, MPPT DC-to-DC converter. For grid tie and conventional application, after having this constant DC voltage, an inverter will be employed to convert it to line frequency AC voltage.

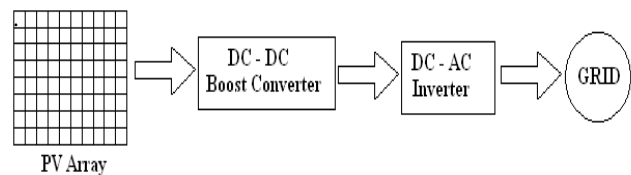


Figure 1. Grid tie PV system topology

The focus of this paper is on the switch mode DC-AC inverter. The control circuits of the proposed inverter will be discussed and the performance of the inverter will be simulated using SIMULINK.

2. Idea of Design

A conventional inverter can be divided into two parts, one is the power circuit part and the other is the control circuit part. The power circuit is normally built up from MOSFET or IGBT switches. The simplest power circuit of the inverter is built up from four switches with two parallel to each other as shown in Figure 2. [2],[3],[5]

These four switches are switching in a predesigned sequence by sending a signal to the gate pin of the switches. This signal is generated by the control circuit. This control circuit is normally built from analog op-amp circuit or digital micro-controller or even combination of both analog and digital. In the inverter design, the control signal is design to be pulse width modulation, PWM in most of the time. However, in some inverter design where the output voltage waveform shape is very importance, the sinusoidal pulse width modulation, SPWM as shown

in Figure 3 is used to reduce the harmonic contain in the output waveform and provide better quality of the output voltage.

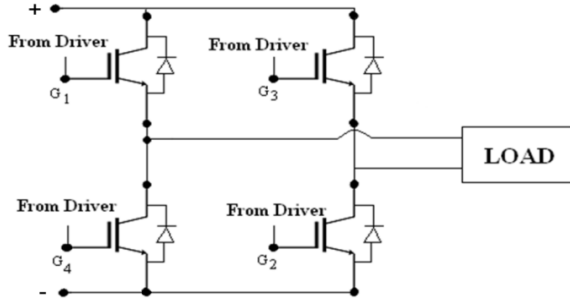


Figure 2. The inverter power circuit

In switching process, the four switches are grouped into two groups, G_1 and G_2 is in one group and G_3 and G_4 is in another. When G_1 and G_2 are switched and G_3 and G_4 is forced to be off by the signal from the control circuit, the output voltage from the inverter will be in positive, however, if G_3 and G_4 are switched and G_1 and G_2 is forced to be off, the inverter will give a negative voltage as the output. [2],[3]

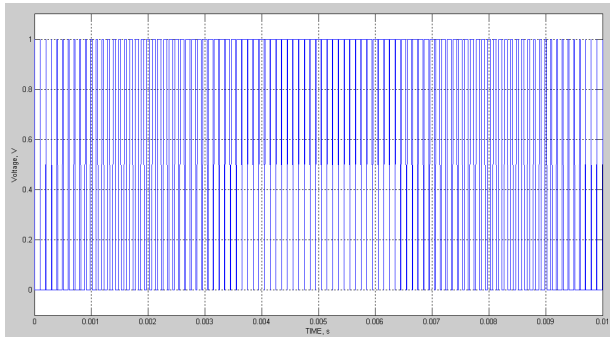


Figure 3. The SPWM signal

For a grid tie inverter or GTI, its design is slightly different from the stand alone inverter or inverter using in variable speed controller. The output voltage of a GTI should obey some requirements so that the inverter can be connected to the grid and sending power into the grid. The rules or requirements that to be obeyed are: [4],[6],[7]

- 1) The voltage magnitude and phase must be equal to the grid.
- 2) The frequency of the output voltage from the inverter must be exactly equal to the grid frequency. (50Hz for Malaysia).

To make sure that the output of the GTI obeys the above rules, the grid voltage is sampled and as a reference for the design of the switching signal.

Besides, the load of the GTI also different from the stand alone or the variable speed controller inverter. The GTI have a load of the power grid where the load contains power much more high compare to the GTI. This require for the GTI to force the power generated from the PV into the grid. The real and reactive power flow of the GTI into the grid is shown in Equation 1 and 2. [4]

$$\text{Real Power, } P = \frac{V_{inv} V_{grid}}{Z_t} \sin \theta \quad \dots (1)$$

$$\text{Reactive Power, } Q = \frac{V_{inv}^2}{Z_t} - \frac{V_{inv} V_{grid}}{Z_t} \cos \theta \quad \dots (2)$$

Where, V_{inv} = Inverter Output Voltage

V_{grid} = Power grid voltage

Z_t = Impedance of the linking line

θ = Angle different between V_{inv} and V_{grid}

From Equation 1, it is very clear that in order to send the maximum real power into the grid, the phase angle should be at 90 degree. However, for the stability limit of the GTI, this angle must not be 90 degree and should be lower than that. However, for Equation 1, the value of \sin can have both positive and negative value. For positive value, output voltage of the inverter leading the grid voltage, the real power will flow from the GTI to the grid but if the value is negative then the real power will flow from the opposite direction.

3. Proposed Design

A. PROPOSED CIRCUIT

The proposed power circuit of the inverter in this paper is as shown in Figure 2 above. This is a very simple inverter design consist of four numbers of IGBT gates, two parallel with each other. For the control circuit, the proposed design will be consisted of combination of analog and digital circuit. The analog circuit will be used to generate the signal require to do the switching of the power circuit and the digital circuit which is consist of the micro-controller that will be act as the brain of the control circuit. The digital circuit will control the generate signals so that the signal will be in sequence.

B. PROPOSED SWITCHING TECHNIQUE

In a conventional inverter design, normally the switching will used only one type of the switching technique only. However, in this proposed design instate of using one type of switching signal to switch the inverter, a combination of SPWM and square wave is used. With this kind of combination switching, the switching loss across the switches of the inverter will be greatly reduced due to reduce of switching frequency.

The block diagram of the proposed switching circuit is as shown in Figure 4.

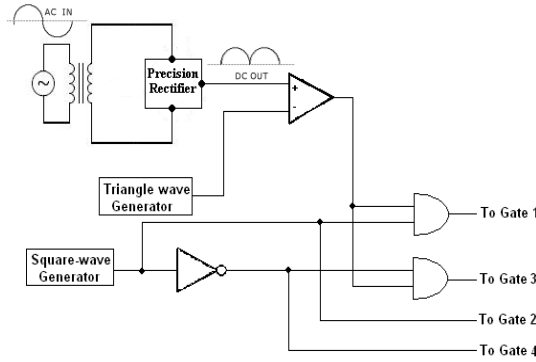


Figure 4. Low power control circuit for proposed design

In order to simplify the synchronizing process, the sine wave of the proposed design will be sampled from the power grid by using the voltage transformer to step down the 230V grid voltage to 5V. With the sine wave sampled from the grid and used to generate the SPWM signal, the frequency of the output voltage from the GTI will be having the same frequency as the grid voltage where this is one of the most important requirement for the GTI as stated in the introduction. After having the sine wave sampled from the grid, this sampled sine wave is rectified by a precision rectifier as shown in Figure 4. The precision rectifier will rectify the sine wave from the grid to waveform as shown in Figure 5.

Besides processing the sampled sine wave, to produce a SPWM signal, a high frequency triangle wave also required. In this proposed design the high frequency triangle wave will also generated by the analog oscillator and the frequency will be 10 kHz as shown in Figure 6.

These two signals will then pass through a comparator to produce SPWM for the switching signal. These two signals will produce a uni-polar SPWM as shown in Figure 3. The uni-polar SPWM signal have only positive value and will be change from +5V to 0V and back to +5V again.

Besides, having the SPWM signal for the switching of the IGBT as many conventional inverter circuits, the proposed GTI will also have square wave signal for the switching. This square wave signal will be in line frequency (50 Hz for Malaysia) and in phase with the SPWM as shown in Figure 7. This square wave signal will pass through a NOT gate and creating another set of signal which is 180° out phase from the original square wave.

After having the SPWM and square wave signal, since the inverter having 4 numbers of MOSFET switches, the inverter require 4 sets of switching signal to control the gates. To produce 4 sets of signal, the two sets of square wave signal are undergo the AND operation with the

SPWM signal and produce 4 set signals as shown in Figure 8 to switch the MOSFET switches. These 4 sets of switching signal can divide into 2 groups. The first group consists of the SIGNAL (A) and SIGNAL (B) from Figure 8. This group will switch the gate G_1 and gate G_2 and at the same time, the gate G_3 and gate G_4 will stay off to provide positive output voltage for the inverter on the first half cycle. The second group consists of SIGNAL (C) and SIGNAL (D) from Figure 8. This group will act on the second half on the cycle by switching gate G_3 and G_4 and at the same time, gate G_1 and G_2 will force to off condition to provide negative voltage at the output of the inverter. The output voltage of the inverter before filtering process is as shown in Figure 9.

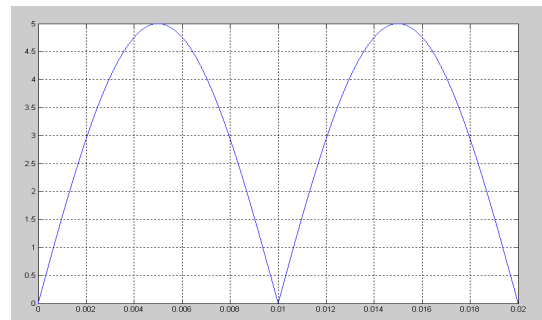


Figure 5. Rectified sine wave

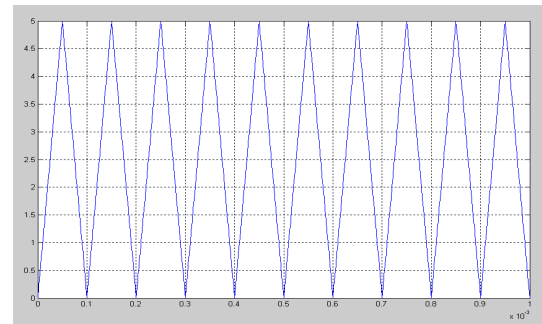


Figure 6. High frequency triangle wave

C. POWER SENDING

As stated in section 2, sending power from the GTI into the power grid needs the voltage angle of the GTI to be leading the grid voltage angle. To able this, a phase shifter circuit is insert into the system. After the sine wave is sampled from the grid, the waveform is passed through the phase shifter to able to make the leading condition. To sending the maximum power to the grid, theoretically the leading angle, as shown in equation 1 must be in 90° . However, in practical, due to the stability issue, will be not be exactly 90° but low then 90° . Figure 10 shown the output real power versus angle different, .

Besides that, the line impedance, Z_l also plays a very importance role in sending power to the power grid. In most of the inverter design, a LC low pass filter will be employed at the output of the inverter to reduce the harmonic influence. This LC filter will increase the Z_l and result in reduce of the maximum power sending as shown in equation 1.

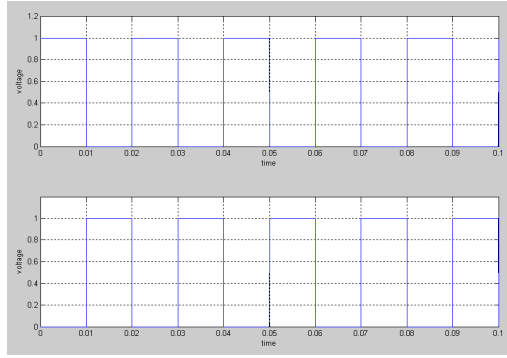


Figure 7. Square wave

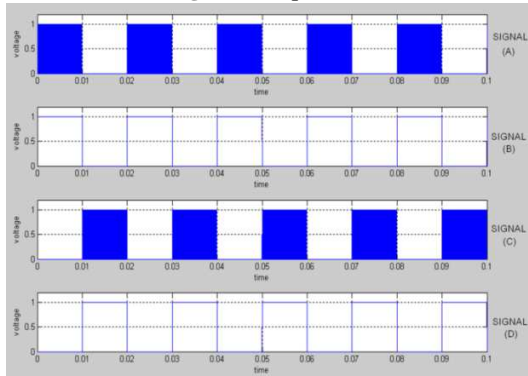


Figure 8. Output of the control circuit

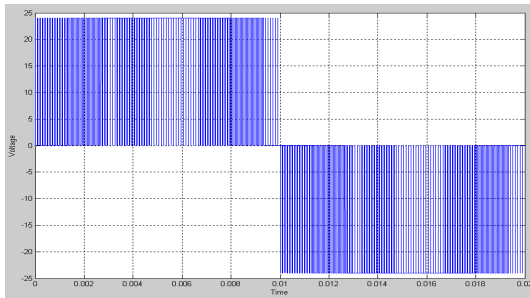


Figure 9. Output of the inverter

D. DESIGN OPERATION

The operation of the proposed inverter can be divided into two parts. The first part is the synchronizing and the second part is the power sending.

On the synchronizing part, the inverter will generate the output in phase with the grid. This is done by sample the sine wave and set the phase shift to zero. This un-

shifted sine wave will be rectified and compare with the high frequency triangle wave to produce the SPWM signal. This SPWM signal will then undergo an AND operation with the square wave and produce 4 set of the signal as shown in figure 8. The first set of signal in figure 8 will switch gate G_1 in figure 2, second set of signal will switch gate G_2 , third set of signal will switch gate G_3 , forth set of signal will switch gate G_4 .

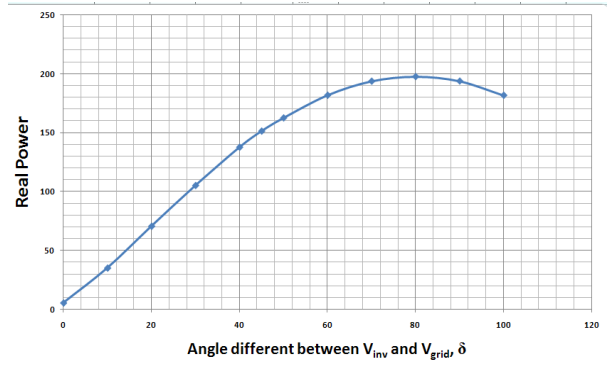


Figure 10. Output real power the inverter again angle different

With this kind of switching and zero phase shifts, the output voltage of the inverter will be controlled with the same phase with the grid. Having the grid and inverter voltage to be in phase, the zero crossing of both voltages is detected. Once the zero crossing is detected, the contactor between the grid and inverter is activated and tie the grid and inverter together.

After both voltages are tied together, the proposed inverter will have to send power to the grid. On this part, the phase shifter at the control circuit of the proposed inverter will shift the inverter output voltage to be leading the grid voltage with an angle of δ . With this angle different the inverter will send real and reactive power as shown in equation 1 and 2.

Besides, to avoid the grid to having power from the inverter when the grid is down and create unnecessary accident, the circuit breaker between the grid and the inverter will break when the grid losing its power and isolate the inverter from the grid. This isolation or islanding process is to avoid the grid to become live part on the time when is should not be. In this process, the circuit breaker is shunt tripped by the relay where this relay will censor the grid with a current transformer (CT). Once the grid loss their power, the relay will active and shunt trip the circuit breaker.

E. OUTPUT VOLTAGE AND HARMONIC

One of the most importance parameter of the designed switching strategy that will influence the performance of the inverter is the amplitude modulation, M_A . The amplitude modulation is define as the ratio between the sine waveform which also know as reference signal V_r , and the triangular waveform also know as carrier signal V_c . Mathematically, the M_A is define as shown in equation 3.

$$M_A = \frac{V_r}{V_c} \quad \dots (3)$$

This M_A play a vary importance role in determine the output voltage of the inverter. Theoretically, as the M_A value increase, the AC output voltage of the inverter also will increase. A simulation using Matlab Simulink had been done to the proposed switching to predict the output voltage with deferent value of M_A . Figure 12 show the graph M_A against the RMS output voltage for the input DC voltage at 24V.

From figure 11, it can conclude that the output RMS voltage will increase as the M_A increase. However, this increase can be categorized into two parts. The first part is when $M_A < 1$ and the second part is when $M_A > 1$. For $M_A < 1$, the increase of the RMS voltage as the M_A increase is large. This is because as the M_A increased, the “ON” duration of the SPWM signal will be longer resulting in the power electronic gates to be in the “ON” state for longer time. Figure 12(a) and 12(b) show the SPWM signal for $M_A=0.1$ and $M_A=0.9$ respectively.

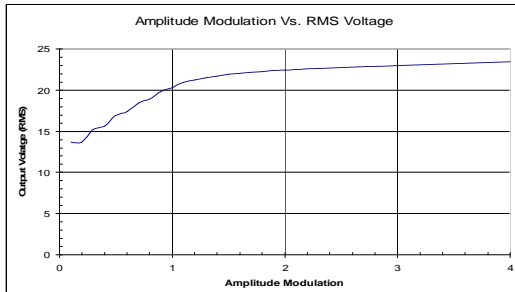


Figure 11. Amplitude Modulation M_A vs. RMS Voltage

For $M_A > 1$, the RMS voltage will still increase as the M_A increase. However, the rate of the increase will be far lower than when M_A is less than 1 and this increase will stop at a voltage level equal to the input DC voltage when the M_A is very large until the SPWM become always in the “ON” level. Figure 13 show the SPWM signal for $M_A=2$.

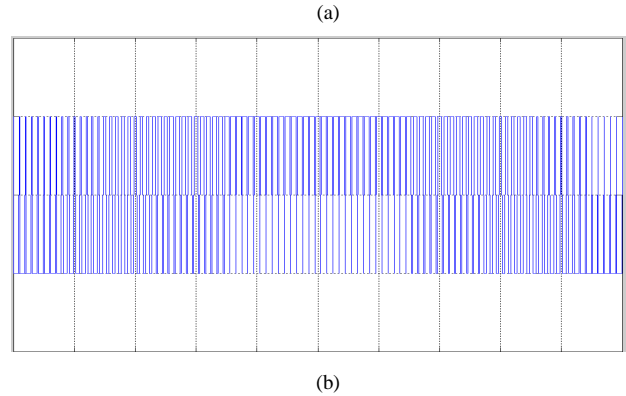
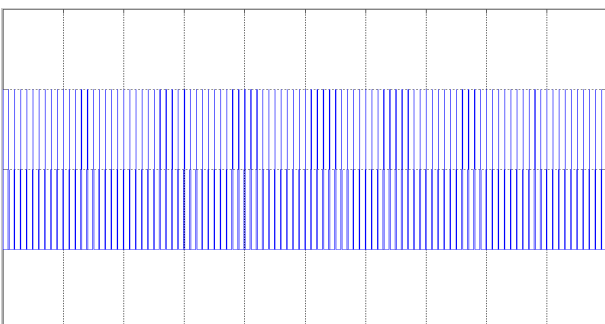


Figure 12. SPWM signal (a) $M_A = 0.1$
(b) $M_A = 0.9$

Besides will influence the output voltage of the inverter, the M_A also will influence the output harmonic. As the M_A increase from 0 to 1, the total harmonic distortion (THD) of the output will decrease. However, thing change when the M_A value increase after the unity value. This can be clearly found out from the result of the simulation using Matlab Simulink showing in figure 14. In figure 14, the THD values show in the graph is the values that not pass through the LC filter.

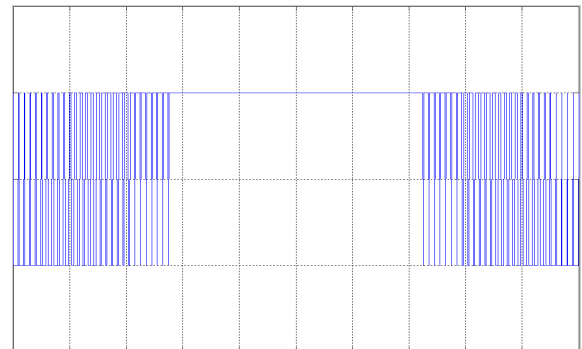


Figure 13. SPWM signal for $M_A = 2$

So, to get the high voltage level with low harmonic contain so that the filter design is more easy, the M_A value is choose in between 0.8 to 0.9. At this M_A value we can get the output voltage to be in 20-21V for the 24V DC input and the voltage THD around 38-39% with any

filtering process. After having the filtering process, the voltage THD will further reduce to much lower level.

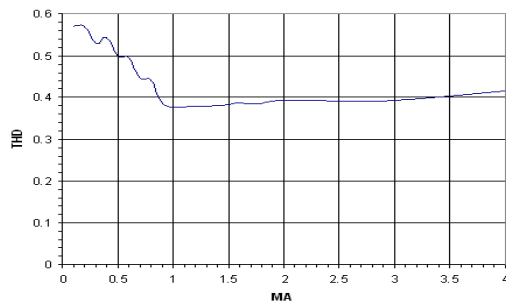


Figure 14. Amplitude Modulation M_A vs. THD

4. Simulation Result

The proposed design will undergo a computer simulation using MATLAB Simulink. In this simulation the switching frequency is fixed at 10 kHz and the output on the inverter is step up using a step up transformer with ratio 15:230. Figure 15 - 18 shown the output of the inverter and the amount of the power send at the leading angle, of 45°

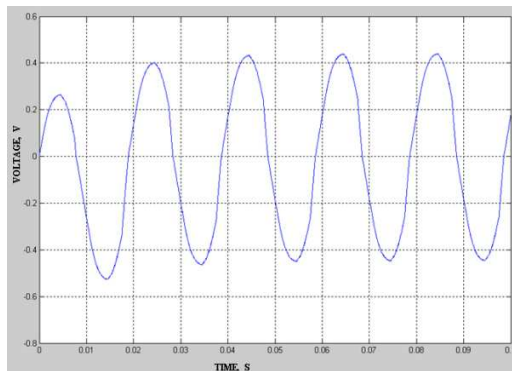


Figure 15. Output Current of the inverter

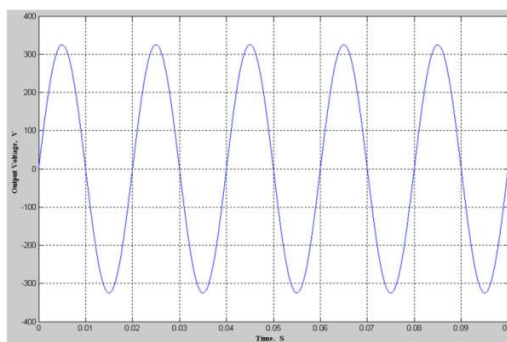


Figure 16. Output Voltage of the inverter

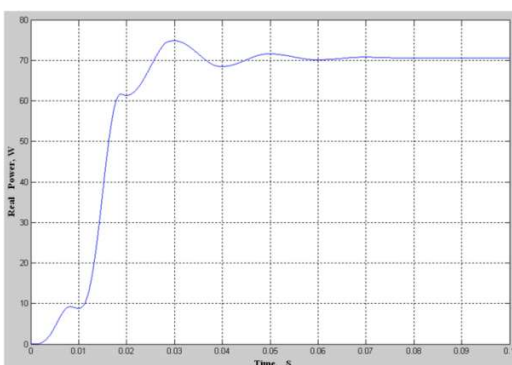


Figure 17. Output Real Power at $= 45^\circ$

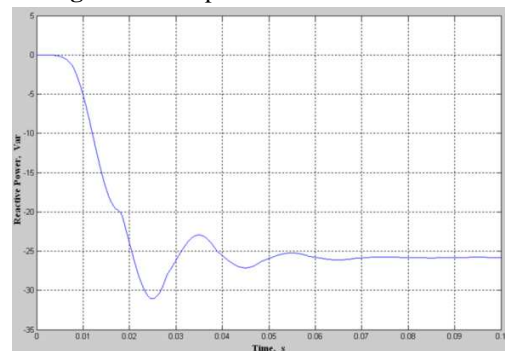


Figure 18. Output Reactive Power at $= 45^\circ$

5. Conclusion

Figure 16- 19, it is clearly find out that the proposed inverter require about 2 cycles (0.04 second for 50 Hz system) to stable after the contactor tie the inverter to the grid. On the transition period, the real and reactive power will fluctuate a lot. The real power will shoot to 75W and come to stable at 70W. For the reactive power, it will over shoot to -32 VAR and stable to -25 VAR. The negative value of the reactive power show that the proposed inverter having a capacitor characteristic, besides sending power to the grid, the inverter can be also used to improve power factor. For the output current shown in figure 11, it will also become stable after 0.02s. The THD of the current is about 5%. Moreover, figure 13 show the output voltage of the inverter that tied to the grid. The output voltage is nearly sinusoidal with the THD less than 0.1% which is very much lower than the IEEE 519. These simulations result clearly shown that the proposed inverter can be applied as a GTI.

REFERENCES

- [1] [HTTP://WWW.EIA.DOE.GOV/OIAF/IEO/ELECTRICITY.HT](http://www.eia.doe.gov/OIAF/IEO/ELECTRICITY.HT) ML (JAN2010)
- [2] Rashid, M. H. (1993). *Power Electronics, Circuits, Devices, and Applications*, Prentice-Hall, Englewood Cliffs.

- [3] Mohan, N., Undeland, T., and Robbins, W., (1995). *Power Electronic: Converters, Applications, and Designs*, 2nd ed., John Wiley & Sons, New York.
- [4] V. Meksarik, S. Masri, S. Taib, and C. M. Hadzer (2003). *Simulation of Parallel-Loaded Resonant Inverter For Photovoltaic Grid Connected*, National Power and Energy Conference (PECon), Malaysia
- [5] Issa Batarseh, (2004). *Power Electronic Circuits*, John Wiley & Sons, Inc.
- [6] Soeren Baekhoej Kjaer, John K. Pedersen, Frede Blaabjerg., (SEPT. 2005). *A Review of Single-Phase Grid-Connected Inverters for Photovoltaic Modules*. IEEE TRANSACTIONS ON INDUSTRY APPLICATION, VOL. 41, No. 5
- [7] Sachin Jain, and Vivek Agarwal, (SEPT 2007). *A Single_stage Grid Connected Inverter Topology for Solar PV System With Maximum Power Point Tracking*, IEEE TRANSACTIONS ON POWER ELECTRONICS, VOL. 22 No. 5